Tensilica Xtensa LX6 Dual core 32-bit

Is the CPU (main microprocessor) of the microcontroller ESP32. It operates at clock frequency up to 240MHz with 2 cores allowing multitasking. It comes built in with Internal memory:

ROM: 448 Kb (For booting and core functions.)

SRAM: 520 Kb (For data and instruction.)

Resources:

* <https://gertech.se/gertech/files/ESP32_Specification.pdf>
* <https://en.wikipedia.org/wiki/ESP32>